IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Docket No. RMI1P021/RZMI-P0320-US
METHOD AND APPARATUS FOR DETERMINING PRECEDENCE IN A
CLASSIFICATION ENGINE
US Application 10/789,667
Filed: 02/27/2004
US Patent 7,213,111
Issued: 05/01/2007
Owner of Record: RMI Corporation

Docket No. RMI1P039/RZMI-P0308-US
METHOD AND APPARATUS FOR PROVIDING INTERNAL TABLE EXTENSIBILITY
WITH EXTERNAL INTERFACE
US Application 10/687,785
Filed: 10/17/2003
US Patent 7,174,441
Issued: 02/06/2007
Owner of Record: RMI Corporation

Docket No. RMI1P043/RZMI-P0318-US
METHOD AND APPARATUS FOR IMPLEMENTING A SEARCH ENGINE USING AN
SRAM
US Application 10/735,107
Filed: 12/12/2003
US Patent 7,234,019
Issued: 06/19/2007
Owner of Record: RMI Corporation

Docket No. RMI1P054/RZMI-P0508-US
AN APPARATUS AND METHOD FOR OPERATING A DUAL PORT MEMORY CELL
US Application 09/277,659
Filed: 03/26/1999
US Patent 6,252,818
Issued: 06/26/2001
Owner of Record: RMI Corporation

Docket No. RMI1P055/RZMI-P0510-US
PROCESSOR WITH MULTIPLE EXECUTION UNITS AND LOCAL AND GLOBAL
REGISTER BYPASSES
US Application 09/281,620
Filed: 03/30/1999
US Patent 6,088,784
Issued: 07/11/2000
Owner of Record: RMI Corporation

Docket No. RMI1P056/RZMI-P0511-US
FLOATING-POINT AND INTEGER MULTIPLY-ADD AND MULTIPLY-ACCUMULATE
US Application 09/235,148
Filed: 01/21/1999
US Patent 6,480,872
Issued: 11/12/2002
Owner of Record: RMI Corporation

Docket No. RMI1P057/RZMI-P0513-US
METHOD AND APPARATUS FOR VECTOR REGISTER WITH SCALAR VALUES
US Application 09/422,045
Filed: 10/20/1999
US Patent 6,530,011
Issued: 03/04/2003
Owner of Record: RMI Corporation

Docket No. RMI1P058/RZMI-P0517-US
SINGLE PHASE EDGE TRIGGER REGISTER
US Application 09/569,818
Filed: 05/12/2000
US Patent 6,388,471
Issued: 05/14/2002
Owner of Record: RMI Corporation

Docket No. RMI1P059/RZMI-P0518-US
HIGH PERFORMANCE METHOD AND SYSTEM FOR PROCESSING INFORMATION ON
AN INTEGRATED CIRCUIT
US Application 09/654,717
Filed: 09/05/2000
US Patent 6,775,788
Issued: 08/10/2004
Owner of Record: RMI Corporation

Docket No. RMI1P060/RZMI-P0519-US
METHOD AND SYSTEM FOR INITIATING COMPUTATION UPON UNORDERED
RECEIPT OF DATA
US Application 09/654,759
Filed: 09/05/2000
US Patent 6,708,282
Issued: 03/16/2004
Owner of Record: RMI Corporation

Docket No. RMI1P061/RZMI-P0520-US
METHOD FOR COORDINATING INFORMATION FLOW BETWEEN COMPONENTS
US Application 09/654,718
Filed: 09/05/2000
US Patent 7,062,767
Issued: 06/13/2006
Owner of Record: RMI Corporation

Docket No. RMI1P063/RZMI-P0522-US
SCALABLE REPLACEMENT METHOD AND SYSTEM IN A CACHE MEMORY
US Application 09/562,062
Filed: 05/01/2000
US Patent 6,694,408
Issued: 02/17/2004
Owner of Record: RMI Corporation

Docket No. RMI1P064/RZMI-P0523-US
METHOD AND SYSTEM FOR REDUCING TAKEN BRANCH PENALTY
US Application 09/562,061
Filed: 05/01/2000
US Patent 6,735,689
Issued: 05/11/2004
Owner of Record: RMI Corporation

Docket No. RMI1P066/RZMI-P0525-US
PIPELINED PROCESSING WITH COMMIT SPECULATION STAGING BUFFER AND
LOAD/STORE CENTRIC EXCEPTION HANDLING
US Application 09/564,715
Filed: 05/03/2000
US Patent 7,188,232
Issued: 03/06/2007
Owner of Record: RMI Corporation

Docket No. RMI1P069/RZMI-P0528-US
REDUCED LINE SELECT DECODER FOR A MEMORY ARRAY
US Application 09/562,058
Filed: 05/01/2000
US Patent 6,252,819
Issued: 06/26/2001
Owner of Record: RMI Corporation

Docket No. RMI1P070/RZMI-P0529-US
DIGITAL PROGRAMMABLE DELAY ELEMENT
US Application 09/562,057
Filed: 05/01/2000
US Patent 6,255,879
Issued: 07/03/2001
Owner of Record: RMI Corporation

Docket No. RMI1P071/RZMI-P0530-US
FLOATING POINT PIPELINE METHOD AND CIRCUIT FOR FAST INVERSE SQUARE
ROOT CALCULATIONS
US Application 09/562,056
Filed: 05/01/2000
US Patent 6,963,895
Issued: 11/08/2005
Owner of Record: RMI Corporation

Docket No. RMI1P072/RZMI-P0531-US
LOW-VOLTAGE CMOS PHASE-LOCKED LOOP (PLL) FOR HIGH-PERFORMANCE
MICROPROCESSOR CLOCK GENERATION
US Application 09/562,055
Filed: 05/01/2000
US Patent 6,292,061
Issued: 09/18/2001
Owner of Record: RMI Corporation

Docket No. RMI1P074/RZMI-P0533-US
DYNAMIC CIRCUIT USING EXCLUSIVE STATES
US Application 09/996,865
Filed: 11/27/2001
US Patent 7,028,069
Issued: 04/11/2006
Owner of Record: RMI Corporation

Docket No. RMI1P075/RZMI-P0534-US
SYSTEM AND METHOD FOR A HIGH SPEED, BI-DIRECTIONAL, ZERO TURNAROUND
TIME, PSEUDO DIFFERENTIAL BUS CAPABLE OF SUPPORTING ARBITRARY
NUMBER OF DRIVERS AND RECEIVERS
US Application 09/909,316
Filed: 07/19/2001
US Patent 6,686,774
Issued: 02/03/2004
Owner of Record: RMI Corporation

Docket No. RMI1P081/RZMI-P0504-US
CIRCUIT, ARCHITECTURE AND METHOD FOR ANALYZING THE OPERATION OF A
DIGITAL PROCESSING SYSTEM
US Application 09/126,680
Filed: 07/30/1998
US Patent 6,311,292
Issued: 10/30/2001
Owner of Record: RMI Corporation

Docket No. RMI1P082/RZMI-P0506-US
CACHE MEMORY CELL WITH A PRE-PROGRAMMED STATE
US Application 08/982,822
Filed: 12/02/1997
US Patent 6,070,229
Issued: 05/30/2000
Owner of Record: RMI Corporation

Docket No. RMI1P082A/RZMI-P0506D-US
CACHE MEMORY CELL WITH A PRE-PROGRAMMED STATE
US Application 09/569,543
Filed: 05/12/2000
US Patent 6,400,599
Issued: 06/04/2002
Owner of Record: RMI Corporation

Docket No. RMI1P083/RZMI-P0505-US
METHOD AND APPARATUS FOR COMMUNICATING SIGNALS BETWEEN CIRCUITS
OPERATING AT DIFFERENT FREQUENCIES
US Application 09/059,614
Filed: 04/13/1998
US Patent 6,092,129
Issued: 07/18/2000
Owner of Record: RMI Corporation

Docket No. RMI1P084/RZMI-P0501-US
METHOD AND APPARATUS FOR DUAL ISSUE OF PROGRAM INSTRUCTIONS TO
SYMMETRIC MULTIFUNCTIONAL EXECUTION UNITS
US Application 08/883,147
Filed: 06/27/1997
US Patent 6,035,388
Issued: 03/07/2000
Owner of Record: RMI Corporation

Docket No. RMI1P084A/RZMI-P0501C-US
METHOD AND APPARATUS FOR DUAL ISSUE OF PROGRAM INSTRUCTIONS TO
SYMMETRIC MULTIFUNCTIONAL EXECUTION UNITS
US Application 09/519,524
Filed: 03/06/2000
US Patent 6,594,753
Issued: 07/15/2003
Owner of Record: RMI Corporation

Docket No. RMI1P085/RZMI-P0502-US
WRITEBACK CACHE CELL WITH A DUAL PORTED DIRTY BIT CELL AND METHOD
FOR OPERATING SUCH A CACHE CELL
US Application 08/924,604
Filed: 09/05/1997
US Patent 6,055,606
Issued: 04/25/2000
Owner of Record: RMI Corporation

Docket No. RMI1P086/RZMI-P0503-US
SYSTEM BUS ARBITRATOR FOR FACILITATING MULTIPLE TRANSACTIONS IN A
COMPUTER SYSTEM
US Application 09/059,615
Filed: 04/13/1998
US Patent 6,085,271
Issued: 07/04/2000
Owner of Record: RMI Corporation

REVOCATION OF PRIOR POWERS OF ATTORNEY
AND GRANT OF NEW POWER OF ATTORNEY

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

As assignee of record of the entire interest of the above-identified patent applications(s) and/or patent(s), all powers of attorney previously given are hereby revoked and the following agents are hereby appointed to prosecute and transact all business in the U.S. Patent and Trademark Office connected therewith.

Richard C. Bridgeman (Reg. No. 57,773)

Please send all correspondence for these applications(s) and/or patent(s) as follows:

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